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CONLEY ROSE, P.C. 5601 Granite Parkway, Suite 750 Plano, Texas 75024-6616 Fax Number: (972) 731-2289

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NAME::

Examiner Abdou K. Seye, Group Art Unit 2194

II S Patent and Trademark Office

FAX:

571 270 2062

FROM:

Michael W. Piper (sent by Jean Brown)

DATE

April 24, 2008

RE:

U.S. Patent Application No. 10/697,417

Amendment to Specification for Examiner's Amendment

REMARKS: Examiner Seye: Would you like this amendment sent to you e-mail so you can copy

and paste it into the Office Action?

Total Number of Pages (Including This One):

OUR CLIENT/MATTER NO .:

4000-16000

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Patent Application No. 10/697,417

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P.02

Atty Docket: IDF 2563 (4000=16000)

Patent

#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Joseph G. Laura

Serial No.: 10/697,417 š

Group Art Unit: 2194

Filed:

October 30, 2003

Examiner: Seve, Abdou K.

For:

System and Method for

00000000

Confirmation No. 7214

Asynchronous Processing in COROL

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

### AMENDMENT TO SPECIFICATION FOR EXAMINER'S AMENDMENT

Applicant thanks Examiner Abdou K. Seve for his telephone call on April 24, 2008. Examiner Seye suggested amendments to paragraph 001 be made to update the application serial number information. Applicant herein authorizes the following amendments to be entered by means of an Examiner's Amendment. The changes made are shown by underlining the added text and surrounding with double brackets the deleted text.

Amendments to the Specification begin on page 2 of this paper.

Attv Docket: IDF 2563 (4000=16000)

Patent

Please delete paragraph [001] in its entirety and replace with the following:

[001] This application is related to U.S. Patent Application Serial No. 10/696,968, now U.S.

Patent No. 7,340,731 issued March 4, 2008 [[\_\_\_\_\_\_\_\_]], entitled SYSTEM AND

METHOD FOR COBOL TO PROVIDE SHARED MEMORY AND MEMORY AND

MESSAGE QUEUES, inventor Joseph G. Laura, filed on even date herewith, U.S. Patent

Application Serial No. 10/696,895, now U.S. Patent No. 7,340,735 issued March 4, 2008 [[\_\_\_\_\_\_\_\_]], entitled IMPLEMENTATION OF DISTRIBUTED AND ASYNCHRONOUS

PROCESSING IN COBOL, inventor Joseph G. Laura, filed on even date herewith, and pending

U.S. Patent Application Serial No. 10/696,828 [[\_\_\_\_\_\_\_]], entitled SYSTEM AND

METHOD FOR DISTRIBUTED PROCESSING IN COBOL, inventor Joseph G. Laura, filed on even date herewith, all of which are incorporated herein by reference for all purposes.

APR-24-2008 15:08 CONLEY & ROSE PC 972 731 2289 P.04

Atty Docket: IDF 2563 (4000-16000)

Patent

#### Conclusion

If the Examiner has any questions or comments or otherwise feels it would be helpful in expediting the application, he is encouraged to telephone the undersigned at (972) 731-2288.

Respectfully submitted,

Date: April 24, 2008

CONLEY ROSE, P.C. 5601 Granite Parkway, Suite 750 Plano, Texas 75024 (972) 731-2288 (972) 731-2289 (facsimile) /Michael W. Piper/ Michael W. Piper Reg. No. 39,800

ATTORNEY FOR APPLICANT

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